

FIGURE 1

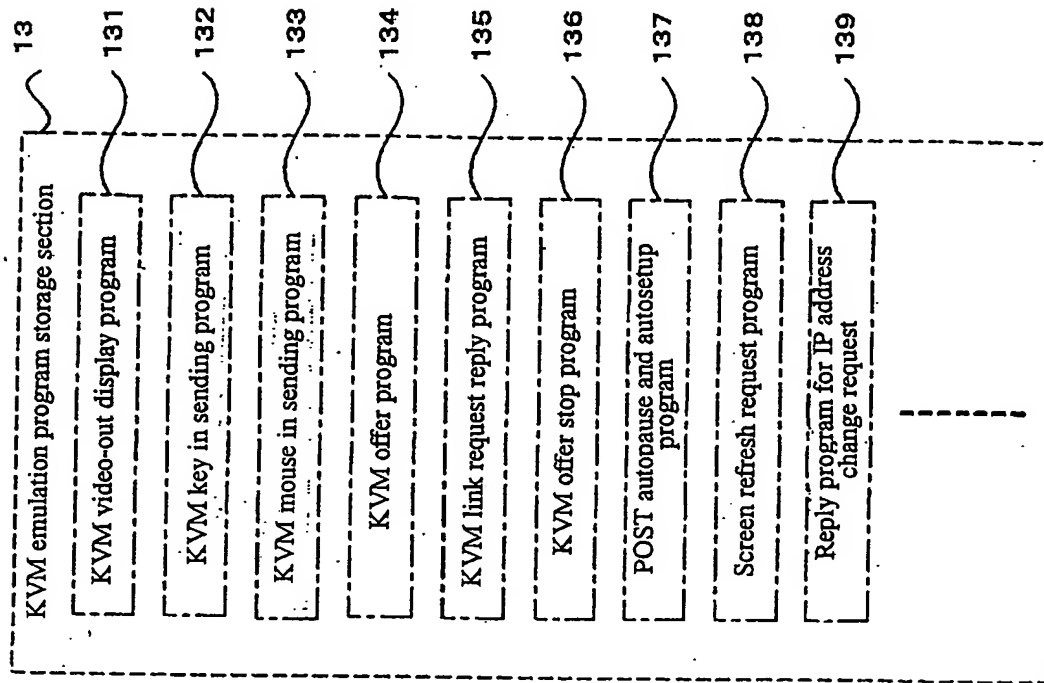


FIGURE 2

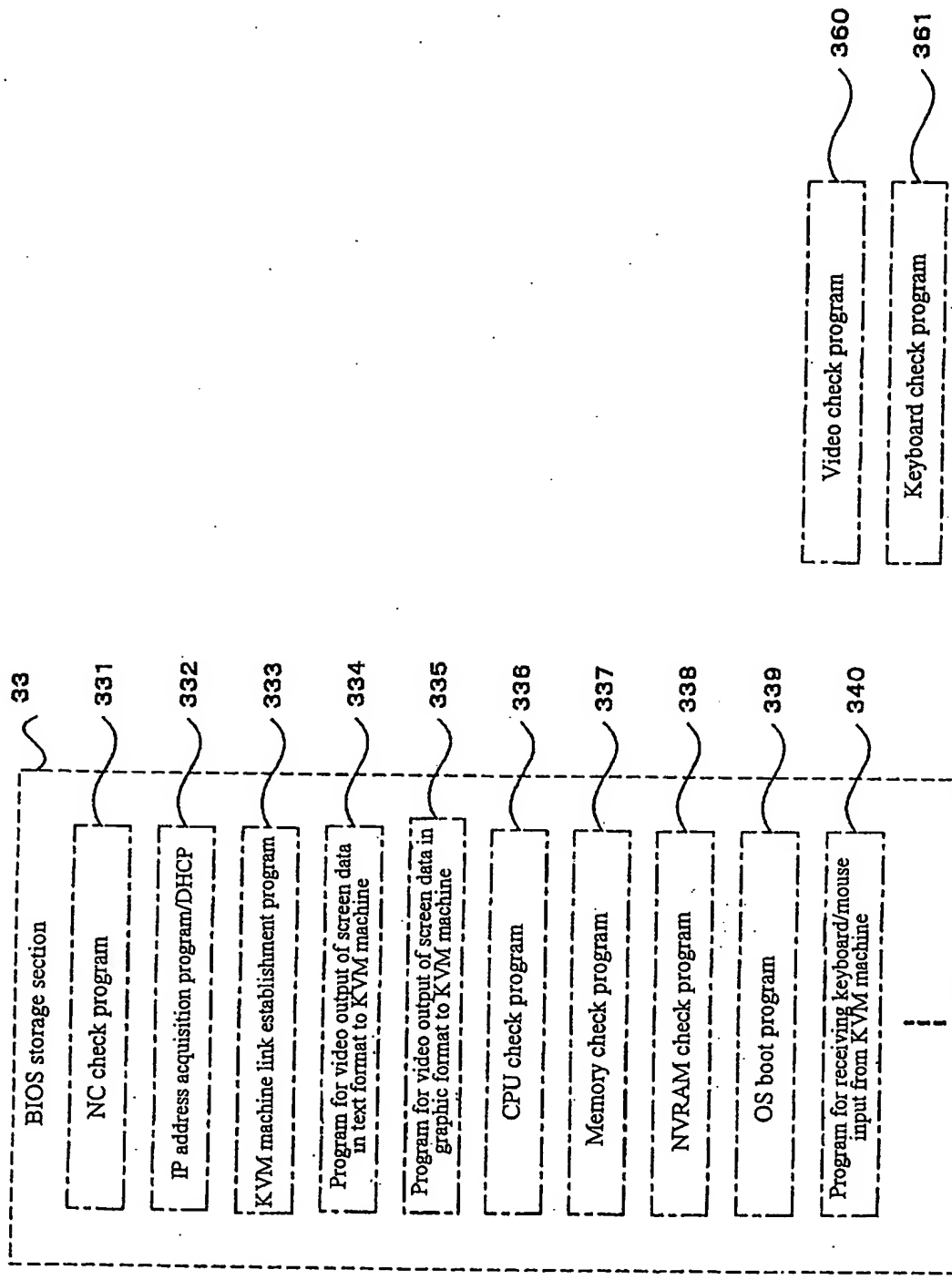


FIGURE 3

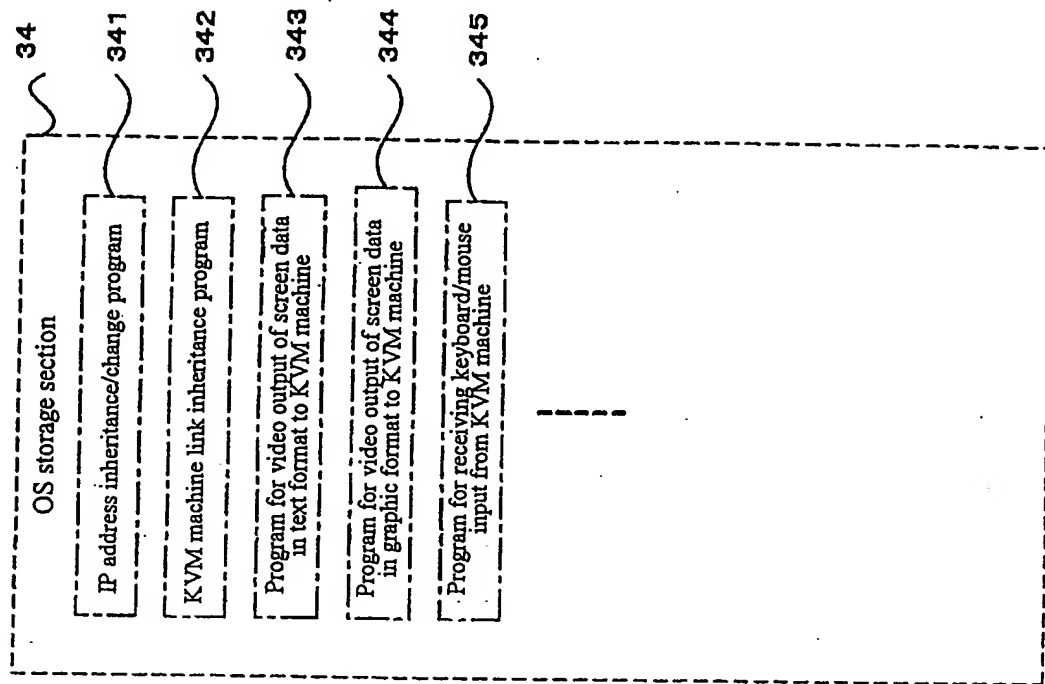


FIGURE 4

Controlled-side information processing device equipped
with no user I/F (POST execution machine)

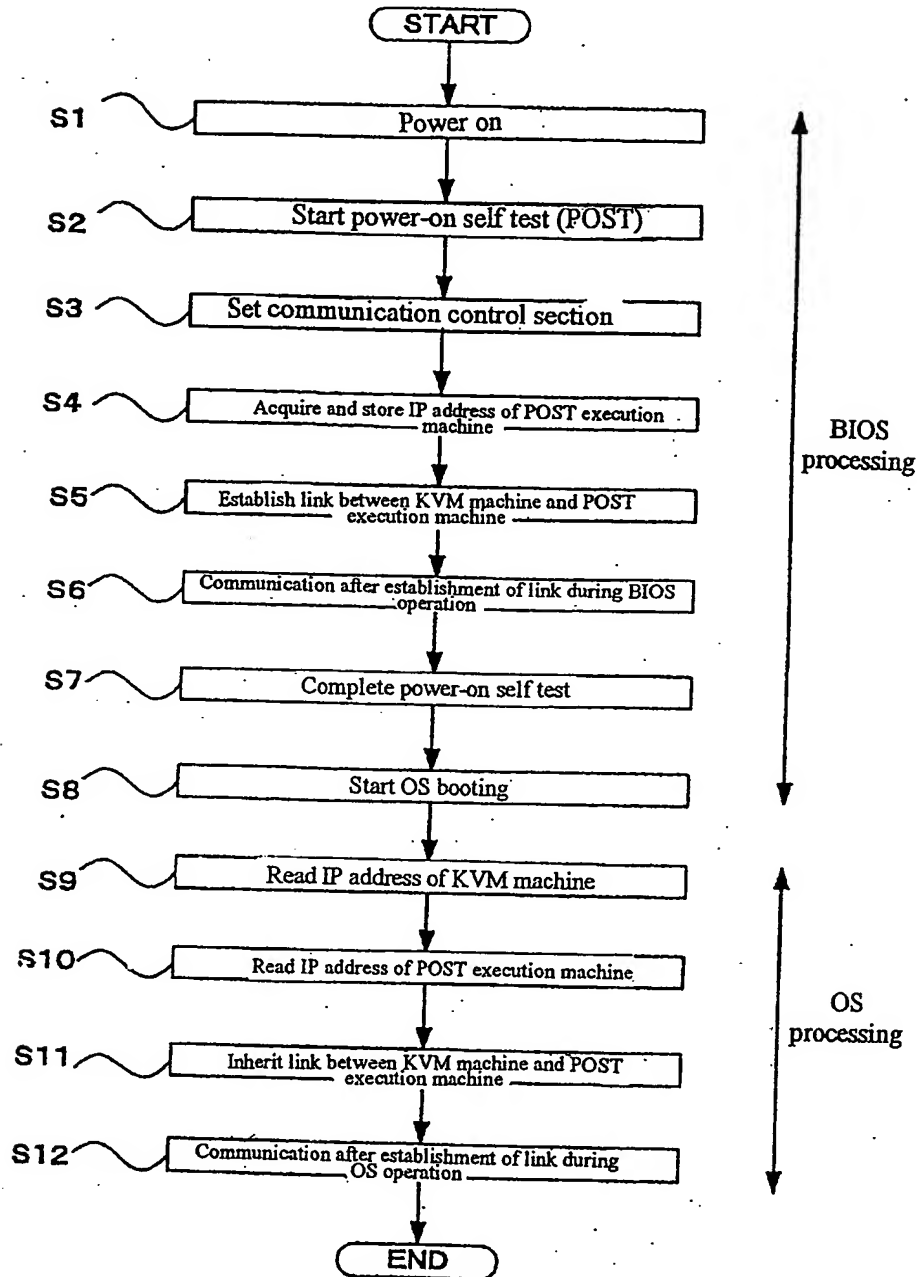


FIGURE 5

Step S3: Details of flow at the time of setting of network control section
(during BIOS operation)

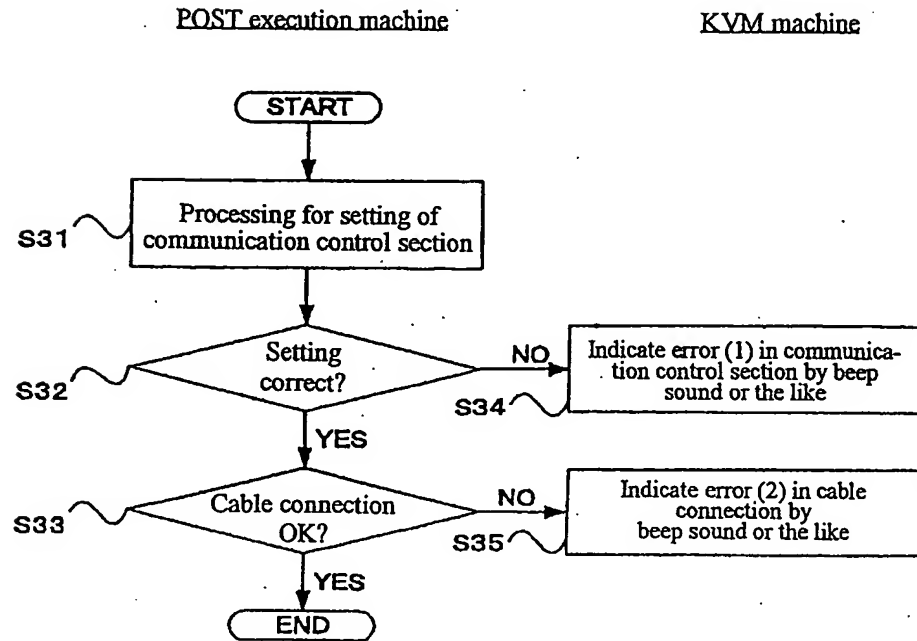


FIGURE 6

Step S4: Details of flow of acquisition and storage of
IP address for POST execution machine

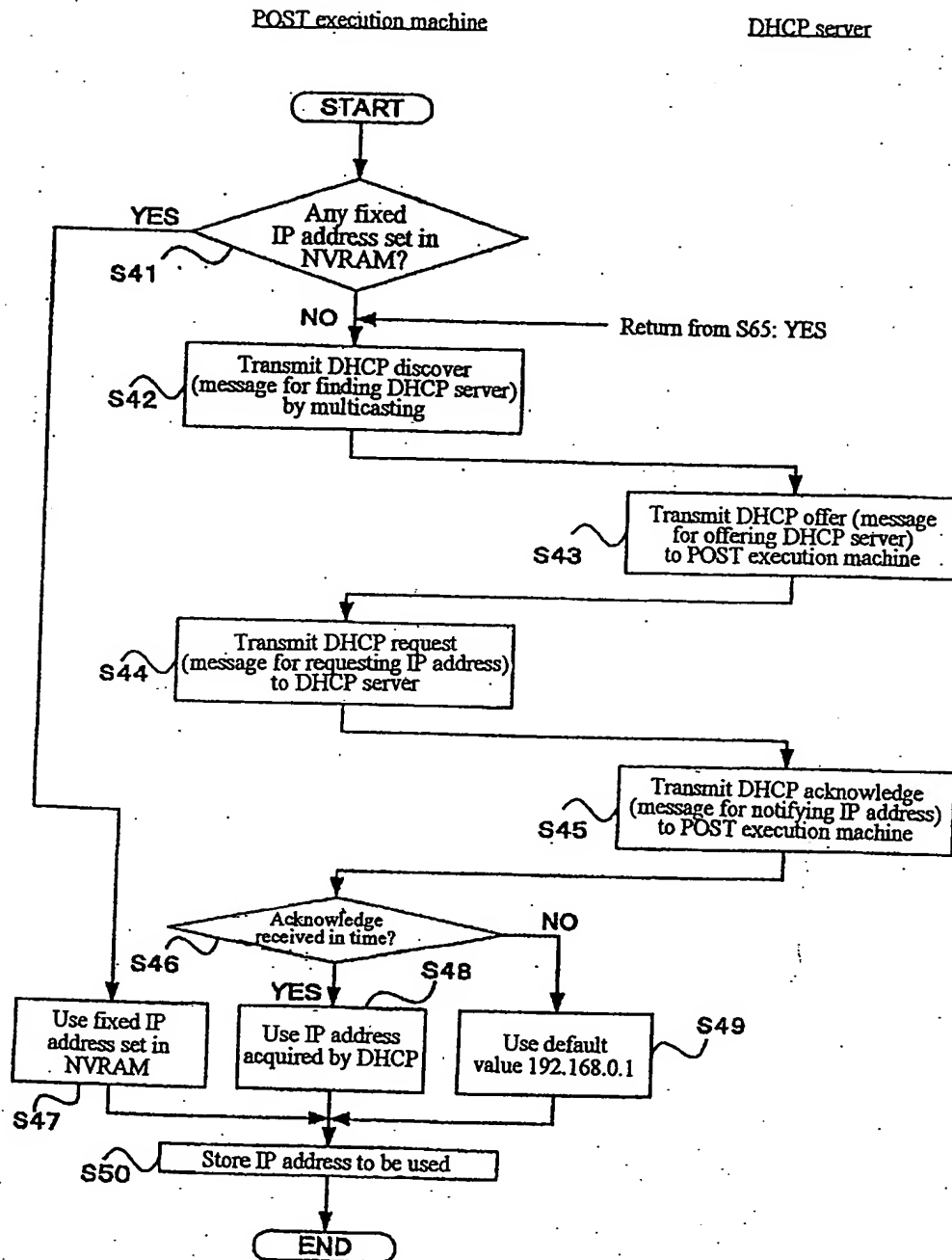


FIGURE 7

Step S5(1/2): Details of flow at the time of establishment of initial link
between POST execution machine and KVM machine (during BIOS operation)

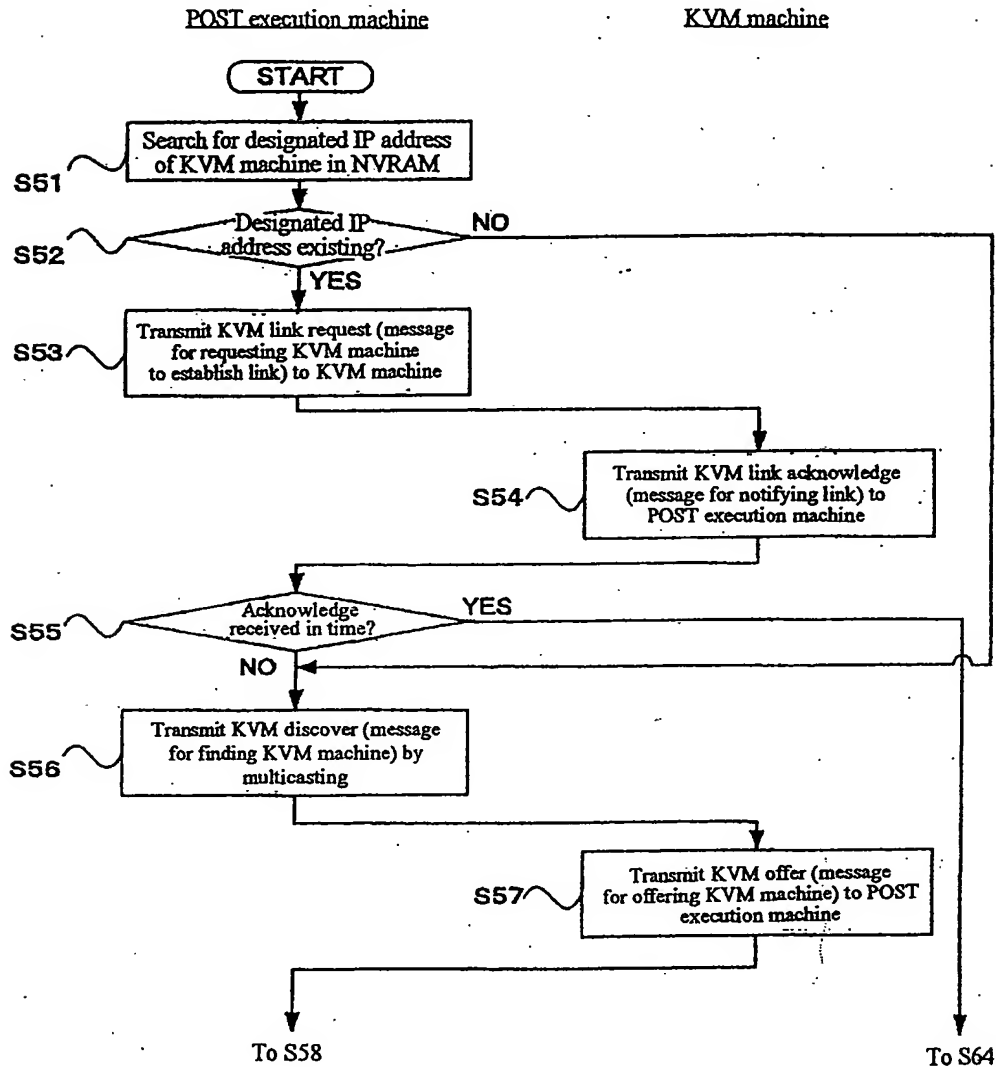


FIGURE 8

Step S5(2/2): details of flow at the time of establishment of initial link between
 POST execution machine and KVM machine (during BIOS operation)

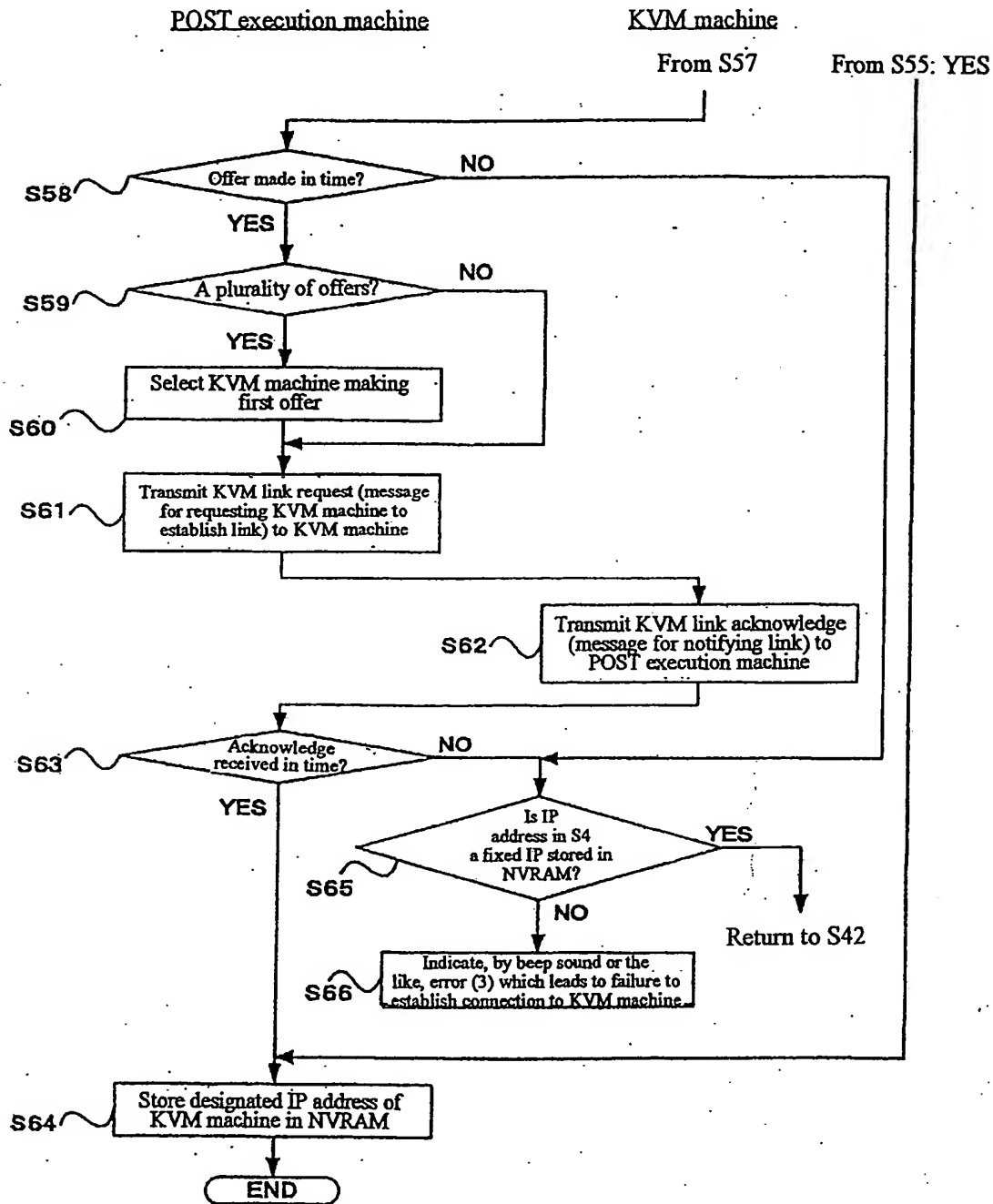


FIGURE 9

Step S6: Details of flow of communication after establishment of link during BIOS operation (communication after establishment of initial link between POST execution machine and KVM machine)

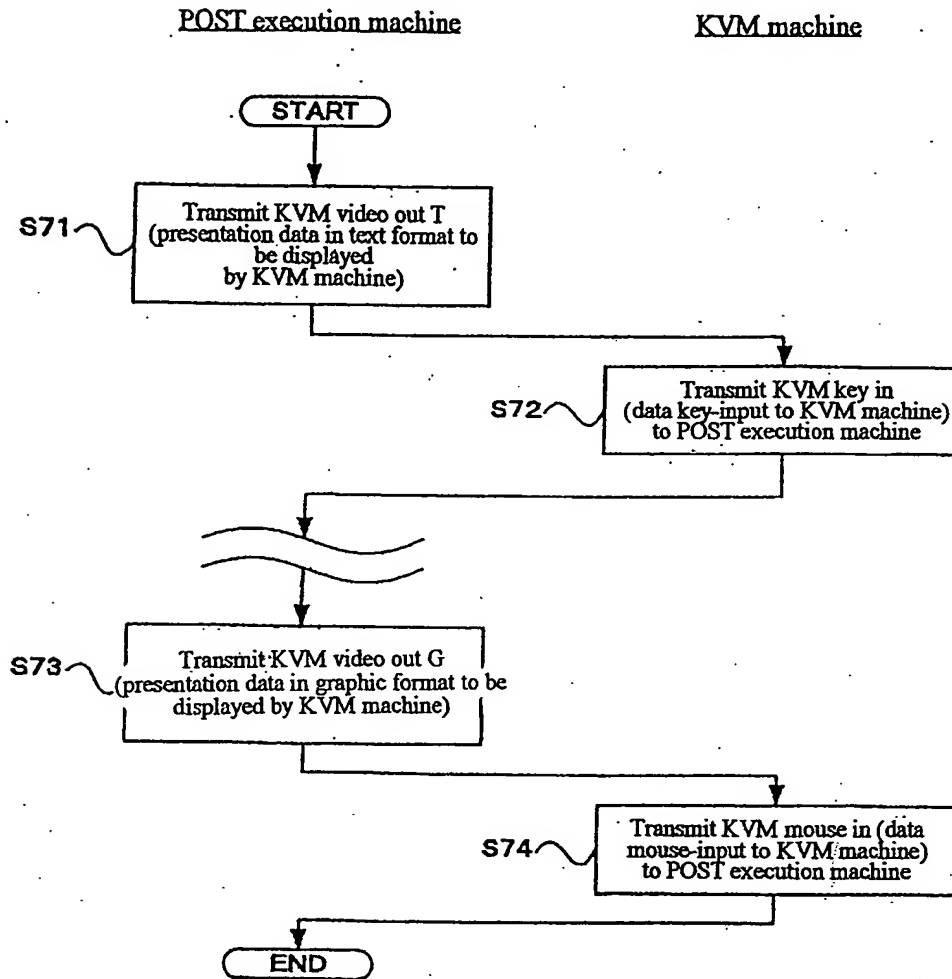


FIGURE 10

Details of flow of refresh request from KVM machine during OS operation
(refresh request after establishment of link between POST execution machine
and KVM machine)

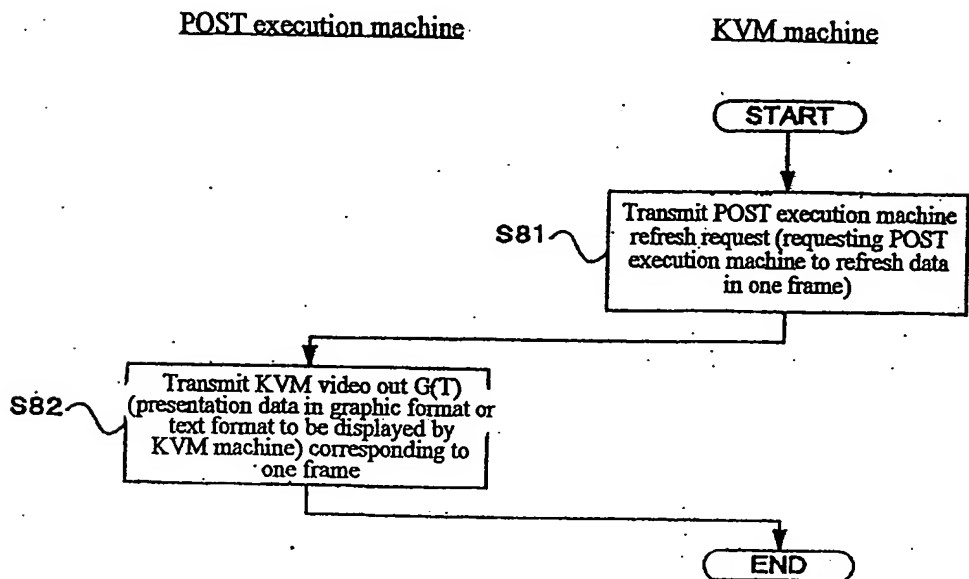


FIGURE 11

Details of flow of changing and checking IP address of POST execution machine

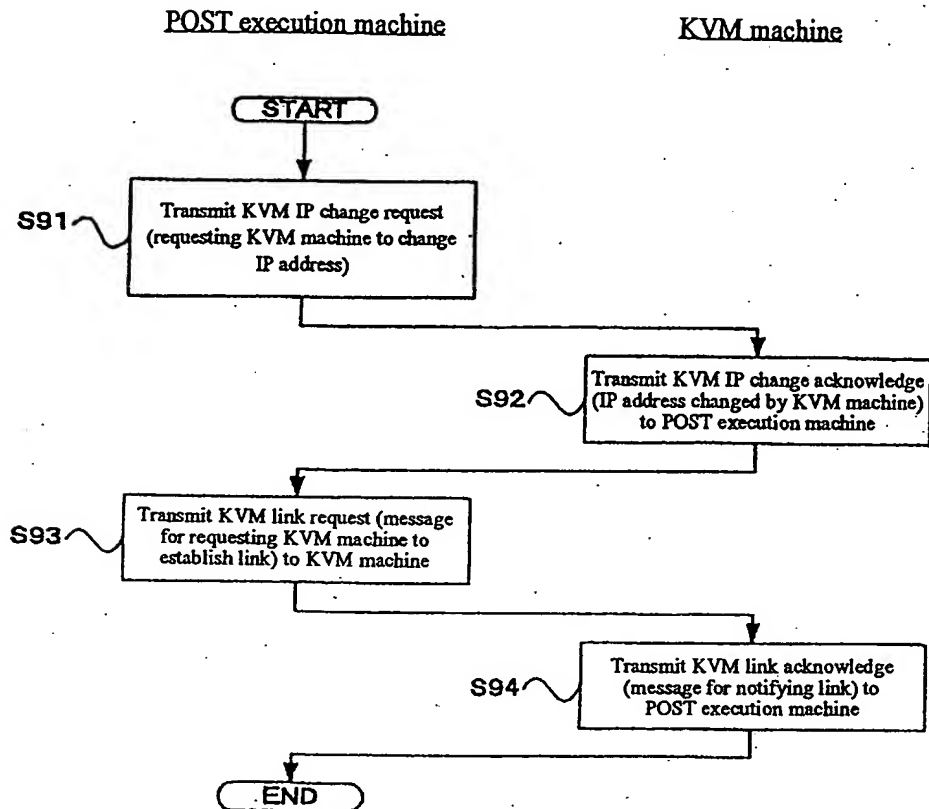


FIGURE 12